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To: Commissioner for Patents for Examiner Carl G. Colin Group Art Unit 2136	Facsimile No. 571/273-8300
From: Jennifer Pilcher Legal Assistant to Wayne Bailey	No. of Pages Including Cover Sheet: 31
<p>Enclosed herewith:</p> <ul style="list-style-type: none">• Transmittal;• Response to Notice of Non-Compliant Appeal Brief;• Supplemental Appeal Brief; and• Copy of Notice of Non-Compliant Appeal Brief.	
Re: Application Serial No. 09/738,243 Attorney Docket No. AUS920000814US1	
Date: Monday, May 08, 2006	
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MAY 08 2006

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: **Leung et al.**

Serial No.: **09/738,243**

Filed: **December 15, 2000**

**For: Method and Apparatus for Dual
Hardware and Software
Cryptography**

§ Group Art Unit: **2136**

§

§ Examiner: **Colin, Carl G.**

§

§ Attorney Docket No.: **AUS920000814US1**

§

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By: 

Jennifer Picher

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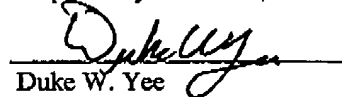
Sir:

ENCLOSED HEREWITH:

- Response to Notice of Non-Compliant Appeal Brief;
- Supplemental Appeal Brief; and
- Copy of Notice of Non-Compliant Appeal Brief.

No fees are believed to be required. If, however, any fees are required, I authorize the Commissioner to charge these fees which may be required to IBM Corporation Deposit Account No. 09-0447. No extension of time is believed to be necessary. If, however, an extension of time is required, the extension is requested, and I authorize the Commissioner to charge any fees for this extension to IBM Corporation Deposit Account No. 09-0447.

Respectfully submitted,



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ATTORNEY FOR APPLICANTS

MAY 08 2006

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application: Leung et al.

Serial No.: 09/738,243

Filed: December 15, 2000

For: Method and Apparatus for Dual
Hardware and Software Cryptography§
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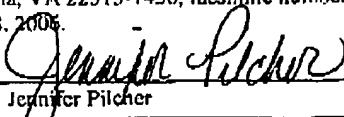
Group Art Unit: 2136

Examiner: Colin, Carl G.

Attorney Docket No.: AUS920000814US1

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on May 8, 2006.
By: 
Jennifer Pilcher

RESPONSE TO NOTICE OF NON-COMPLIANT APPEAL BRIEFCommissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

A Notice of Non-Compliant Appeal Brief was received by Applicant essentially stating that the Appeal Brief filed on January 30, 2006 is considered non-compliant because of problems associated with the concise explanation of each independent and problems associated with setting forth structure associated with means-plus-function elements. A copy of the Notice of Non-Compliant Appeal Brief is attached hereto.

No fees are believed to be required. If, however, any fees are required, I authorize the Commissioner to charge these fees which may be required to IBM Corporation Deposit Account No. 09-0447. No extension of time is believed to be necessary. If, however, an extension of time is required, the extension is requested, and I authorize the Commissioner to charge any fees for this extension to IBM Corporation Deposit Account No. 09-0447.

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REMARKS

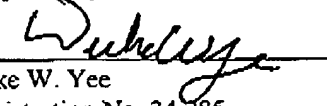
The Examiner notes that (1) with respect to each independent claim, a concise explanation is not provided with reference to page and line number; and (2) with respect to the means-plus-function claims, structure is not properly identified for each means-plus-function element.

Applicants are filing concurrently herewith a Supplemental Appeal Brief, in which (1) the concise explanation for Claims 1 and 8 in the Summary of Claimed Subject Matter section has been modified to more particularly reference the page, line number and drawing reference; and (2) the structure identification for Claims 26 and 33 in the Summary of Claimed Subject Matter section has been modified to specifically reference corresponding structure for each individual means-plus-function element.

It is therefore respectfully submitted that all noted deficiencies have been overcome by the filing of such Supplemental Appeal Brief.

Date: May 8, 2006

Respectfully submitted,



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Docket No. AUS920000814US1

MAY 08 2006

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Leung et al.

Serial No. 09/738,243

Filed: December 15, 2000

For: Method and Apparatus for Dual
Hardware and Software
Cryptography§
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Group Art Unit: 2136

Examiner: Colin, Carl G.

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450Certificate of Transmission Under 37 C.F.R. § 1.8(a)

I hereby certify this correspondence is being transmitted via
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Alexandria, VA 22313-1450, facsimile number (571) 273-8300
on May 8, 2006.

By:


Jennifer Pilcher

SUPPLEMENTAL APPEAL BRIEF (37 C.F.R. 41.37)

This brief is in furtherance of the Notice of Appeal, filed in this case on November 28, 2005
and the Notice of Non-Compliant Appeal Brief mailed on April 20, 2006.

The fees required under § 41.20(B)(2), and any required petition for extension of time for
filing this brief and fees therefore, are dealt with in the accompanying TRANSMITTAL OF
SUPPLEMENTAL APPEAL BRIEF.

(Supplemental Appeal Brief Page 1 of 24)
Leung et al. - 09/738,243

REAL PARTY IN INTEREST

The real party in interest in this appeal is the following party: International Business Machines Corporation of Armonk, N.Y.

RELATED APPEALS AND INTERFERENCES

With respect to other appeals or interferences that will directly affect, or be directly affected by, or have a bearing on the Board's decision in the pending appeal, there are no such appeals or interferences.

STATUS OF CLAIMS**A. TOTAL NUMBER OF CLAIMS IN APPLICATION**

Claims in the application are: 1-46

B. STATUS OF ALL THE CLAIMS IN APPLICATION

1. Claims canceled: 5-7, 11, 15-18, 20-25, 30-32, 36, 40, 41 and 44-46
2. Claims withdrawn from consideration but not canceled:
3. Claims pending: 1-4, 8-10, 12-14, 19, 26-29, 33-35, 37-39, 42 and 43
4. Claims allowed: none
5. Claims rejected: 1-4, 8-10, 12-14, 19, 26-29, 33-35, 37-39, 42 and 43
6. Claims objected to: none

C. CLAIMS ON APPEAL

The claims on appeal are: 1-4, 8-10, 12-14, 19, 26-29, 33-35, 37-39, 42 and 43

STATUS OF AMENDMENTS

No amendment after final was filed for this case.

SUMMARY OF CLAIMED SUBJECT MATTER

Cryptography is a technique for keeping information secure, where information is enciphered (scrambled) so that it is difficult to determine the meaning without the appropriate decipher key(s). Cryptographic operations can be performed using either hardware-based or software-based solutions, with the choice of which type of system to use typically being based on various trade-offs such as cost, speed of operation and susceptibility to system compromise or malicious hacking. The present invention is generally directed to a technique for dynamically choosing between a hardware and software-based solution for performing cryptographic operations, with a corresponding key conversion to convert the key to a form suitable for use by the selected hardware or software-based solution.

A. CLAIM 1 - INDEPENDENT

Claim 1 is specifically directed to a method in a data processing system for executing cryptographic operations. Responsive to a request to perform a cryptographic operation, a dynamic selection is made between a software process and a hardware process for performing the cryptographic operation based on a policy (Specification page 14, lines 17-31; Figure 4, blocks 400, 402 and 404). The cryptographic operation, which is the encryption of data using a key, is performed using the dynamically selected process (Specification page 15, lines 4-6 and 10-12; Figure 4, blocks 408 and 412). The key is converted to a form useable by the selected process if the key is in an unusable form by the selected process (Specification page 15, lines 1-2 and lines 7-9; Figure 4, blocks 406 and 410), where the key is a software key and the selected process is the hardware process and the step of converting the key comprises converting the software key into a hardware form useable by the hardware process for performing the cryptographic operation (Specification page 15, lines 7-12; Figure 4, blocks 410 and 412).

B. CLAIM 8 - INDEPENDENT

Claim 8 is specifically directed to a method in a data processing system for executing cryptographic operations. Responsive to a request to perform a cryptographic operation, a dynamic selection is made between a software process and a hardware process for performing the

cryptographic operation based on a policy (Specification page 14, lines 17-31; Figure 4, blocks 400, 402 and 404). The cryptographic operation, which is the encryption of data using a key, is performed using the dynamically selected process (Specification page 15, lines 4-6 and 10-12; Figure 4, blocks 408 and 412). The key is a hardware key and is converted into a software form useable by the software process for performing the cryptographic operation (Specification page 15, lines 1 – 6; Figure 4, blocks 406 and 408).

C. CLAIM 26 – INDEPENDENT AND MEANS-PLUS-FUNCTION

Claim 26 is a system claim corresponding to method Claim 1, and the summary of Claim 1 is applicable for Claim 26, and thus is hereby incorporated by reference.

Claim 26 is also a means-plus-function claim. The structure corresponding to the selection means is described at Specification page 6, lines 4 – 8 and at Specification page 6, line 28 – page 9, line 24, and shown by reference character 100 in Figure 1 and reference character 200 in Figure 2; and the structure corresponding to the performing means is also described at Specification page 6, lines 4 – 8 and at Specification page 6, line 28 – page 9, line 24, and shown by reference character 100 in Figure 1 and reference character 200 in Figure 2. In essence, both the cited means-for elements of Claim 26 are performed by the data processing system 100, as described at Specification page 6, lines 4-8 and page 5, lines 11-14, with the details of such data processing system being described at Specification page 6, line 28 – page 9, line 24.

D. CLAIM 33 – INDEPENDENT AND MEANS-PLUS-FUNCTION

Claim 33 is a system claim corresponding to method Claim 8, and the summary of Claim 8 is applicable for Claim 33, and thus is hereby incorporated by reference.

Claim 33 is also a means-plus-function claim. The structure corresponding to the selection means is described at Specification page 6, lines 4 – 8 and at Specification page 6, line 28 – page 9, line 24, and shown by reference character 100 in Figure 1 and reference character 200 in Figure 2; and the structure corresponding to the performing means is also described at Specification page 6, lines 4 – 8 and at Specification page 6, line 28 – page 9, line 24, and shown by reference character 100 in Figure 1 and reference character 200 in Figure 2. In essence, both the cited means-for elements of Claim 33 are performed by the data processing system 100, as

described at Specification page 6, lines 4-8 and page 5, lines 11-14, with the details of such data processing system being described at Specification page 6, line 28 – page 9, line 24.

E. CLAIM 42 – INDEPENDENT

Claim 42 is a system claim corresponding to method Claim 8, and the summary of Claim 8 is applicable for Claim 42, and thus is hereby incorporated by reference.

F. CLAIM 43 – INDEPENDENT

Claim 43 is a system claim corresponding to method Claim 1, and the summary of Claim 1 is applicable for Claim 43, and thus is hereby incorporated by reference.

GROUND OF REJECTION TO BE REVIEWED ON APPEAL**A. GROUND OF REJECTION 1 (Claims 1-4, 8-10, 12-14, 19, 26-29, 33-35, 37-39 and 42-43)**

Claims 1-4, 8-10, 12-14, 19, 26-29, 33-35, 37-39 and 42-43 stand rejected under 35 U.S.C. § 103 as being unpatentable over US Patent 5,778,072 to Samar in view of US Patent 6,393,565 to Lockhart et al.

ARGUMENT

A. GROUND OF REJECTION 1 (Claims 1-4, 8-10, 12-14, 19, 26-29, 33-35, 37-39 and 42-43)

Claims 1-4, 8-10, 12-14, 19, 26-29, 33-35, 37-39 and 42-43 stand rejected under 35 U.S.C. § 103 as being unpatentable over US Patent 5,778,072 to Samar in view of US Patent 6,393,565 to Lockhart et al. Appellants show error in such rejection as follows.

A.1. Claims 1-4, 19, 26-29 and 43

With respect to Claim 1, such claim recites the claimed feature of “wherein the key is a software key and the selected process is the hardware process and further comprising converting the software key into a hardware form useable by the hardware process for performing the cryptographic operation”. As can be seen, the key that is used for encryption of data is a software key *that is converted to a hardware form* useable by the hardware process for performing cryptographic operations. None of the cited references teach or suggest any type of key conversion at all. For example, the cited Samar reference teaches use of either a smartcard or a software process for performing a cryptographic operation, based upon whether a smartcard is present (Figure 2, blocks 205, 207 and 221). If a smartcard is present, the user is authenticated by initiating a challenge/response protocol, or a particular password/personal identification number protocol, with the user’s smartcard (col. 6, lines 6-11; Figure 2, blocks 207 and 135). The smart card will return a value to the operating system indicating whether the authentication was successful (col. 6, lines 17-19). There is no type of key conversion in this process, as *the key is secretly maintained within the smartcard such that it cannot be externally accessed* (col. 6, lines 50-54). If a smartcard is not present in the system, the user is authenticated with any of the selected encryption services 129 (col. 6, lines 36-38). Since in this scenario no smartcard is present, the key store manager requests (from the selected encryption service) a private key for the user. This private key is written to the user information file (col. 6, lines 40-46). In this scenario of no smartcard being present, there also is no type of key conversion of a software key into a hardware form usable by the hardware process, as this scenario describes use of a software key by a software process. Quite simply, there is no teaching or suggestion of converting a

software key into a hardware form useable by a hardware process for performing a cryptographic operation of encrypting data using such converted key.

For the encryption operation described by Samar at col. 7, lines 10-44, the data to be encrypted is passed to the smartcard where it is encrypted using a “non-readably stored key” (col. 7, lines 30-31), *such that the user’s private key is never exposed outside the card* (col. 7, lines 37-44). Again, there is no type of key conversion performed, as *the cited Samar reference is keen on maintaining a preexisting hardware encryption key internal to the smartcard, which is not readable or otherwise accessible outside the card* (col. 2, lines 19-27; col. 6, lines 50-54; col. 7, lines 37-44).

The Examiner cites three Samar passages as evidencing the conversion of a software key to a form usable by the hardware encryption process: (1) col. 5, line 65 – col. 5, line 6 (Appellant’s note – it is believed this is a typographical error and that the Examiner meant to cite col. 4, line 65 – col. 5, line 6 as the cited passage of col. 5, line 65 – col. 5, line 5 is unclear/confusing); (2) col. 7, lines 32-33; (3) col. 8, lines 30-37; and (4) col. 2, lines 28-35. Appellants will now discuss each of these cited passages individually.

- (1) The passage cited at col. 5, line 65 – col. 5, line 6 (again, it is assumed this passage was intended to be col. 4, line 65 – col. 5, line 5) describes operations performed by a key store manager, including processing requests to create public or private key data using an encryption service, writing such key data to a file/storage, obtaining existing private key data from either a smart card or user information file/storage and processing encryption requests. Of critical importance is that this cited passage does not describe, teach or otherwise suggest any type of key conversion process, and thus does not teach or otherwise suggest the claimed feature of “wherein the key is a software key and the selected process is the hardware process and the step of converting the key comprises converting the software key into a hardware form useable by the hardware process for performing the cryptographic operation”. Rather, Samar’s use of a smartcard as a hardware process for performing cryptographic operations uses a preexisting hardware encryption key maintained internal to the smartcard (col. 2, lines 19-27; col. 6, lines 50-54; col. 7, lines 37-44).
- (2) The passage cited at col. 7, lines 32-33 contains an obvious typographical error, and the word ‘can’ should really be ‘cannot’, and this typographical error is clear when

this cited passage is read in context. As can be seen at col. 7, lines 30-31, it states that the smart card encrypts the data using user A's private key which is *non-readably stored* therein. It is clear that storing something non-readably means that it is stored so that it *cannot* be read. This is also clear by the Samar passage at col. 6, lines 50-54, where it states:

From the foregoing it can be seen that where the user has a smart card 123 **the user's private key is never exposed to the system 100**, and thus cannot be compromised, even if the system 100 is compromised during or after the authentication process.

and at col. 7, lines 37-44 where it states:

Because the private key operation is performed entirely within the smart card 123, **user A's private key is never exposed to the computer 101, and never resides in the addressable memory 103, the storage device 113, or any other publically available facility**. Thus, even if the system 100 had been previously compromised, or is subsequently compromised, user A's private key would still be secure.

Appellants thus urge that this use of the word 'can' is a typographical error which should instead read as 'cannot' when viewed in the proper context, and thus this passage does not establish any ability to read or otherwise access the smart card's internally maintained key for any type of key conversion operation.

- (3) The passage cited at Samar col. 8 describes a *decryption* operation, whereas Claim 1 is directed to an *encryption* operation, and thus these teachings are not germane to any type of claimed *encryption* elements. In any event, this passage does not describe any type of key conversion being performed as a part of an encryption process.

- (4) The passage cited at Samar col. 2 does not teach or otherwise suggest any type of key conversion, and thus does not teach or otherwise suggest the specific claimed feature of “wherein the key is a software key and the selected process is the hardware process and the step of converting the key comprises converting the software key into a hardware form useable by the hardware process for performing the cryptographic operation”.

The Examiner appears to acknowledge such teaching/suggestion deficiency, by stating:

“The use of Lockhart in the rejection is to fully support *the key conversion already disclosed by Samar*. Examiner respectfully asserts that Samar *either alone or in combination with Lockhart* discloses the amended claimed limitations.” (emphasis added by Appellants)

Appellants urge that this ‘hedge’ statement is evidence unto itself that the Examiner is not themselves certain that Samar teaches any type of key conversion, and as shown above Samar does not disclose any type of key conversion of a software key into a hardware form useable by the hardware process for encrypting data. Appellants will now show that the cited Lockhart reference does not overcome such teaching/suggestion deficiency.

Lockhart describes a smart card having a limited history file that can only maintain a limited number of historic or old keys. Accordingly, when a new key is received and needs to be saved in the smart card, the oldest history key is stored in a separate overflow memory such that the newly received key can be saved in the smartcard (col. 4, lines 2-13). There is no description of any type of key conversion of a software key into a hardware form useable by the hardware process for encrypting data. The only described functions performed on the actual key itself are generating a new key, sending a key, receiving a key and storing a key (col. 3, line 54 – col. 4, line 13 and col. 4, line 46 – col. 5, line 3).

The cited Lockhart reference also describes the decrypting of newly received encrypted keys, and the encrypting of old keys for archival storage (col. 4, line 46 – col. 5, line 12). Such encryption and decryption of keys does not teach or otherwise suggest the claimed feature of “wherein the key is a software key and the selected process is the hardware process and the step

of converting the key comprises converting the software key into a hardware form useable by the hardware process for performing the cryptographic operation" as expressly recited in Claim 1. Rather, this encryption of a key is done as a precursor step to archiving the key.

Nor does the cited Lockhart reference teach any conditional operation being performed based upon whether the key is in an unusable form by the selected process. Claim 1 expressly recites a step of "converting the key to a form useable by the selected process *if the key is in an unusable form by the selected process*" (emphasis added), the selected process being a hardware process. Lockhart's smartcard merely stores keys, and has no hardware encryption capability (col. 3, lines 8-19; Figure 2, element 102). The cryptographic operations are instead performed by the cryptographic data manager 104 (Figure 3, elements 202, 206, 232 and 234). Importantly, this cited reference only teaches use of a single encryptor 206, so *there would have been no motivation to conditionally convert between different types of encryption keys (hardware and software) based on the type of encryption process being used as there is but a single encryption process*. The fact that a prior art device could be modified so as to produce the claimed device is not a basis for an obviousness rejection unless the prior art suggested the desirability of such a modification. *In re Gordon*, 733 F.2d 900, 221 USPQ 1125 (Fed. Cir. 1984). There is simply no suggestion or other motivation to modify the teachings of Lockhart to perform the conditional key conversion step when encrypting data, as described above, and thus Claim 1 is further shown to have been erroneously rejected under 35 U.S.C. 103.

It is thus urged that Claim 1 has been erroneously rejected, as there are missing claimed features not taught or suggested by any of the cited references¹.

A.2. Claims 8, 10, 12-14, 33, 35, 37-39 and 42

With respect to Claim 8, such claim recites performing the cryptographic operation using the selected process, where the cryptographic operation is an encryption of data using a key, where the key is a hardware key and the selected process is the software process and further comprising converting the hardware key into a software form useable by the software process for performing the cryptographic operation. As described above with respect to Claim 1, the cited

¹ To establish prima facie obviousness of a claimed invention, all of the claim limitations must be taught or suggested by the prior art. MPEP 2143.03. See also, *In re Royka*, 490 F.2d 580 (C.C.P.A. 1974). If the examiner fails to establish a prima facie case, the rejection is improper and will be overturned. *In re Fine*, 837 F.2d 1071,

Samar reference does not teach any type of key conversion from one form to another, and in fact the reference is keen on not allowing hardware keys to be used with or accessed by other types of cryptographic operations due to security concerns (col. 6, lines 50-54; col. 7, lines 37-44). It is thus urged that Claim 8 is not obvious in view of the cited references, as there are missing claimed features not taught or suggested by any of the cited references.

A.3. Claims 9 and 34

Applicants initially show error in the rejection of Claim 9 (and similarly for Claim 34) for reasons given above with respect to Claim 8 (of which Claim 9 depends upon).

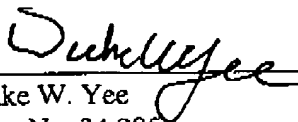
Further with respect to Claim 9 (and similarly for Claim 34), such claim further defines the policy which is used to determine which process (hardware or software) is to be used for performing the encryption of data, and is specifically directed to a policy that comprises a set of rules used to *minimized available resources consumed in performing the cryptographic operation*. In rejecting Claim 9, the Examiner states that such claimed feature is taught by Samar at col. 3, line 45 – col. 4, line 7. Appellants urge that this cited passage describes various features of the Samar invention, including (1) logging out a user's session if they remove the smart card (col. 3, lines 45-52), (2) use of existing applications to take advantage of either the smart card or host encryption services (col. 3, lines 53-62), and (3) an ability to choose between use of the smart card or host encryption services based upon the type of user using the system (col. 3, line 63 – col. 4, line 7). While item (3) does mention criteria that is used to determine which of the smart card or host encryption services to use, such passage does not teach or otherwise suggest *a set of rules that is used to minimize available resources* that are consumed in performing the cryptographic operation. Rather, this cited passage merely states that the choice is made based on the *type of user*. Thus, the Examiner has failed to establish a prima facie showing of obviousness with respect to Claim 9, and accordingly the burden has not shifted to Applicant to rebut such improper obviousness assertion². In addition, as a prima facie case of

¹ 074, 5 USPQ2d 1596, 1598 (Fed. Cir. 1988).

² In rejecting claims under 35 U.S.C. Section 103, the examiner bears the initial burden of presenting a prima facie case of obviousness. *In re Oetiker*, 977 F.2d 1443, 1445, 24 USPQ2d 1443, 1444 (Fed. Cir. 1992). Only if that burden is met, does the burden of coming forward with evidence or argument shift to the applicant. *Id.*

obviousness has not been established with respect to Claim 9, such claim has thus been erroneously rejected³.

In conclusion, Appellants have shown error in the final rejection of all pending claims, and respectfully requests that the Board reverse such final rejection.


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³ *In re Fine, supra.*

CLAIMS APPENDIX

The text of the claims involved in the appeal are:

1. A method in a data processing system for executing cryptographic operations, the method comprising:

responsive to a request to perform a cryptographic operation, dynamically selecting between one of a software process and a hardware process within the data processing system for performing the cryptographic operation based on a policy, to form a selected process; and

performing the cryptographic operation using the selected process, wherein the cryptographic operation is an encryption of data using a key, and wherein the step of performing the cryptographic operation includes converting the key to a form useable by the selected process if the key is in an unusable form by the selected process, wherein the key is a software key and the selected process is the hardware process and the step of converting the key comprises converting the software key into a hardware form useable by the hardware process for performing the cryptographic operation.
2. The method of claim 1, wherein the policy includes selecting the one based on available resources to perform the cryptographic operation.
3. The method of claim 1, wherein the policy includes selecting the one resulting in a fastest completion of the cryptographic operation.

4. The method of claim 1, wherein the selecting step includes:
selecting the one using a preference associated with the request.
8. A method in a data processing system for executing cryptographic operations, the method comprising:
responsive to a request to perform a cryptographic operation, dynamically selecting between one of a software process and a hardware process within the data processing system for performing the cryptographic operation based on a policy, to form a selected process; and
performing the cryptographic operation using the selected process, wherein the cryptographic operation is an encryption of data using a key, wherein the key is a hardware key and the selected process is the software process and further comprising:
converting the hardware key into a software form useable by the software process for performing the cryptographic operation.
9. The method of claim 8, wherein the policy comprises a set of rules used to minimize available resources consumed in performing the cryptographic operation.
10. The method of claim 8, wherein the policy comprises a set of rules used to maximize a speed at which the cryptographic operation is performed.
12. The method of claim 8, wherein the cryptographic operation is one of a message digest and a public-private key encryption.

13. The method of claim 8, wherein the request is received from an application.
14. The method of claim 13, wherein the request is received from the application using an application program interface call made by the application.
19. The method of claim 2, wherein the available resources include available processing resources and memory.
26. A data processing system for executing cryptographic operations, the data processing system comprising:
- selecting means for dynamically selecting between one of a software process and a hardware process within the data processing system for performing a cryptographic operation based on a policy, to form a selected process in response to a request to perform the cryptographic operation; and
 - performing means for performing the cryptographic operation using the selected process, wherein the cryptographic operation is an encryption of data using a key, and wherein the performing means includes converting means for converting the key to a form useable by the selected process if the key is in an unusable form by the selected process, wherein the key is a software key and the selected process is the hardware process and the converting means comprises means for converting the software key into a hardware form useable by the hardware process for performing the cryptographic operation.

27. The data processing system of claim 26, wherein the policy includes selecting the one based on available resources to perform the cryptographic operation.

28. The data processing system of claim 26, wherein the policy includes selecting the one resulting in a fastest completion of the cryptographic operation.

29. The data processing system of claim 26, wherein the selecting means includes:
selecting means for selecting the one using a preference associated with the request.

33. A data processing system for executing cryptographic operations, the data processing system comprising:

selecting means for dynamically selecting between one of a software process and a hardware process within the data processing system for performing a cryptographic operation based on a policy, to form a selected process in response to a request to perform the cryptographic operation; and

performing means for performing the cryptographic operation using the selected process, wherein the cryptographic operation is an encryption of data using a key, and wherein the performing means includes converting means for converting the key to a form useable by the selected process if the key is in an unusable form by the selected process, wherein the key is a hardware key and the selected process is the software process and the converting means comprises means for converting the hardware key into a software form useable by the software process for performing the cryptographic operation.

34. The data processing system of claim 33, wherein the policy comprises a set of rules used to minimize available resources consumed in performing the cryptographic operation.

35. The data processing system of claim 33, wherein the policy comprises a set of rules used to maximize a speed at which the cryptographic operation is performed.

37. The data processing system of claim 33, wherein the cryptographic operation is one of a message digest and a public-private key encryption.

38. The data processing system of claim 33, wherein the request is received from an application.

39. The data processing system of claim 38, wherein the request is received from the application using an application program interface call made by the application.

42. A data processing system comprising:

a bus system;

a communications unit connected to the bus, wherein data is sent and received using the communications unit;

a memory connected to the bus system, wherein a set of instructions are located in the memory; and

a processor unit connected to the bus system, wherein the processor unit executes the set of instructions to (i) dynamically select between one of a software process and a hardware

process within the data processing system for performing a cryptographic operation based on a policy, to form a selected process; (ii) perform the cryptographic operation using the selected process, wherein the cryptographic operation is an encryption of data using a key, wherein the key is a hardware key and the selected process is the software process; and (iii) convert the hardware key into a software form useable by the software process for performing the cryptographic operation.

43. A data processing system comprising:

a bus system;

a communications unit connected to the bus, wherein data is sent and received using the communications unit;

a memory connected to the bus system, wherein a set of instructions are located in the memory; and

a processor unit connected to the bus system, wherein the processor unit executes the set of instructions to (i) dynamically select between one of a software process and a hardware process within the data processing system for performing a cryptographic operation based on a policy, to form a selected process; (ii) perform the cryptographic operation using the selected process, wherein the cryptographic operation is an encryption of data using a key, wherein the key is a software key and the selected process is the hardware process; and (iii) convert the software key into a hardware form useable by the hardware process for performing the cryptographic operation.

EVIDENCE APPENDIX

There is no evidence to be presented.

RELATED PROCEEDINGS APPENDIX

There are no related proceedings.



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DATE	ACTION DOCKETED
<u>4-26-06</u>	<u>Notice of non</u>
<u>5-19-06</u>	<u>Completed Appeal</u>
Docketed By: <u>[Signature]</u>	Date: <u>4-26-06</u>
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Attorney Initials: _____	Date: _____

MAY 08 2006

Notification of Non-Compliant Appeal Brief (37 CFR 41.37)	Application No. 09/738,243	Applicant(s) LEUNG ET AL.	
	Examiner Carl Colin	Art Unit 2136	

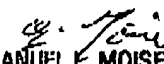
--The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

The Appeal Brief filed on 30 January 2006 is defective for failure to comply with one or more provisions of 37 CFR 41.37.

To avoid dismissal of the appeal, applicant must file an amended brief or other appropriate correction (see MPEP 1205.03) within **ONE MONTH or THIRTY DAYS** from the mailing date of this Notification, whichever is longer.
EXTENSIONS OF THIS TIME PERIOD MAY BE GRANTED UNDER 37 CFR 1.136.

1. ☐ The brief does not contain the items required under 37 CFR 41.37(c), or the items are not under the proper heading or in the proper order.
2. ☐ The brief does not contain a statement of the status of all claims, (e.g., rejected, allowed, withdrawn, objected to, canceled), or does not identify the appealed claims (37 CFR 41.37(c)(1)(iii)).
3. ☐ At least one amendment has been filed subsequent to the final rejection, and the brief does not contain a statement of the status of each such amendment (37 CFR 41.37(c)(1)(iv)).
4. ☒ (a) The brief does not contain a concise explanation of the subject matter defined in each of the independent claims involved in the appeal, referring to the specification by page and line number and to the drawings, if any, by reference characters; and/or (b) the brief fails to: (1) identify, for each independent claim involved in the appeal and for each dependent claim argued separately, every means plus function and step plus function under 35 U.S.C. 112, sixth paragraph, and/or (2) set forth the structure, material, or acts described in the specification as corresponding to each claimed function with reference to the specification by page and line number, and to the drawings, if any, by reference characters (37 CFR 41.37(c)(1)(v)).
5. ☐ The brief does not contain a concise statement of each ground of rejection presented for review (37 CFR 41.37(c)(1)(vi)).
6. ☐ The brief does not present an argument under a separate heading for each ground of rejection on appeal (37 CFR 41.37(c)(1)(vii)).
7. ☐ The brief does not contain a correct copy of the appealed claims as an appendix thereto (37 CFR 41.37(c)(1)(viii)).
8. ☐ The brief does not contain copies of the evidence submitted under 37 CFR 1.130, 1.131, or 1.132 or of any other evidence entered by the examiner and relied upon by appellant in the appeal, along with a statement setting forth where in the record that evidence was entered by the examiner, as an appendix thereto (37 CFR 41.37(c)(1)(ix)).
9. ☐ The brief does not contain copies of the decisions rendered by a court or the Board in the proceeding identified in the Related Appeals and Interferences section of the brief as an appendix thereto (37 CFR 41.37(c)(1)(x)).
10. ☒ Other (including any explanation in support of the above items):

See Continuation Sheet.


EMMANUEL E. MOISE
 SUPERVISORY PATENT EXAMINER

CC

Continuation Sheet (PTOL-462)

Application No. 09/738,243

Continuation of 10. Other (including any explanation in support of the above items): On pages 8-9 of the appeal brief, Appellant does not provide a concise explanation of each of the independent claims involved in the appeal with reference to the specification by page and line number. Appellant, on the other hand, cited one general section (page 14, line 11-page 15, line 12, figure 4, all blocks) to refer to all the claim limitations of the independent claims. Therefore, a concise explanation is not provided with reference to page and line number. In addition, the brief fails to identify for each independent claim and dependent claim argued separately every means plus function and step plus function under 35 USC 112, sixth paragraph and set forth the structure, material or acts described in the specification as corresponding to each claimed function with reference to the specification by page and line number and to the drawings if any by reference characters. Appellant, on the other hand, made a general allegation that all the means plus function and structure corresponding to each and means for elements is described at specification on page 6, line 28 to page 9, line 24 and character 200 in figure 2 (char 200 is a data processing system).